TGB2000A, GPS Baseband Chip Receiver

Overview

- GPS baseband, and navigation S/W
- High accuracy with adaptive processing against a variety of signal level and variation

 Integrated signal p Resource sharing Low power structure Matured proprieta 	techniques and course and managem ry CDM technolo	avigation S/W against multi onfigurable architecture ents ogy	path environments	TGB200 TGB200 L0FP1601 4K31364 1027	D A
Features					
 High Performance GPS Navigation Adaptive operation for high sensitivity and dynamics Advanced tracking loop and filter techniques against multipath environments Patented fast acquisition architecture SBAS support Real-time navigation Multiple Environments Support Stationary, land vehicle, high dynamic airborne and military area Multiple Environments Support NMEA & TGB protocol Satellites and PVT information Customer configuration and masks Test and monitoring 			 A-GPS Support Fast position acquirement with aiding information Architecture Highlights Stand-alone GPS architecture ARM processor embedded BBR and RTC for hot start and navigation parameters storage Various peripherals with 2 UARTs, 2 I2Cs, 2 SPIs, 2 general purpose timers, 1 watchdog timer, 1PPS, interrupt controller, and 10 GPIOs Optional external memory (Flash or SRAM) I/F for special purpose applications 		
Specifications Position Accuracy Autonomous CEP SBAS TTFF ² Hot Cold Aiding Sensitivity Acquisition Navigation Tracking	> < 2m < 1.5m< 1s< 30s< 1s-145dBm-158dBm-162dBm	 Receiver Tracking signal L1 C/A, SBAS Channels 57 Max update rate 2 Hz Protocol support NMEA, TGB binary Power Autonomous Power TBD mW Power save TBD mW Sleep 10 uA 		 Size Package type Package dimension Operating Condition Temp. range 	LQFP 23x23 mm ² -40 ~ 85 °C
Interface					
• GPS RF front-end • RF clock : 16.368	chip interface MHz or 16.36766	57MHz			

- RTC clock : XTAL 32.768KHz
- External interface : UART, SPI, I2C, GPIO, EMI